**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Claim 1 (currently amended): A method for configuring an integrated device in a first processor comprising:

decoding a memory configuration access within a decoder of a second processor, the second processor coupled to the first processor, to a configuration cycle;

routing the configuration cycle to a chipset based at least in part on a routing information; and

forwarding the configuration cycle to configure the integrated device from an unconfigured state to a configured state.

Claim 2 (original): The method of claim 1 wherein the configuration cycle is routed to the chipset via a network fabric.

Claim 3 (currently amended): The method of claim [[1]] 2 wherein the network fabric is a plurality of point to point links.

Claim 4 (currently amended): The method of claim 1 wherein the chipset has a bridge and adheres to ~~a PCI type~~ an interconnect that is ~~either PCI or PCI Express~~ in accordance with a predetermined protocol.

Claim 5 (original): The method of claim 2 wherein the second processor is coupled to the first processor via the network fabric.

Claim 6 (currently amended): A method for configuring an integrated device in a first processor comprising:

decoding an Input Output (IO) configuration access within a second processor, coupled to a first processor, to a configuration cycle; and

routing the configuration cycle to the integrated device based at least in part on a routing information to configure the integrated device from an unconfigured state to a configured state.

Claim 7 (original): The method of claim 6 wherein the configuration cycle is routed to the integrated device via a network fabric.

Claim 8 (currently amended): The method of claim [[6]] 7 wherein the network fabric is a plurality of point to point links.

Claim 9 (currently amended): The method of claim 6 wherein the configuration adheres to ~~a PCI type~~ an interconnect of a predetermined protocol.

Claim 10 (currently amended): The method of claim [[6]] 9 wherein the predetermined protocol comprises a PCI type interconnect protocol ~~is either PCI or PCI Express~~.

Claim 11 (original): The method of claim 7 wherein the second processor is coupled to the first processor via the network fabric.

Claim 12 (original): A processor comprising:

a decoder to decode either a memory or IO configuration access to a configuration cycle; and

to transmit the configuration cycle to either a chipset or integrated device.

Claim 13 (currently amended): The processor of claim 12 wherein the transmission of the configuration cycle to either [[a]] the chipset or integrated device is via a PCI type interconnect ~~that is either PCI or PCI Express~~.

Claim 14 (currently amended): The processor of claim 12 wherein the configuration cycle is to be routed to the integrated device or chipset via a network fabric.

Claim 15 (currently amended): A system comprising:

a first processor with [[an]] a decoder coupled to a second network component with an integrated device, the decoder to decode either a memory or IO configuration access to a configuration cycle; and

to transmit the configuration cycle to either a chipset or the integrated device, wherein the configuration cycle adheres to a first ~~PCI~~ type of interconnect protocol.

Claim 16 (currently amended): The system of claim 15 wherein the ~~PCI~~ first type of interconnect protocol comprises a ~~is either~~ PCI ~~or PCI Express~~ type protocol.

Claim 17 (currently amended): The system of claim 15 wherein the configuration cycle is routed to the integrated device or the chipset via a network fabric.

Claim 18 (currently amended): An article of manufacture comprising:

a machine-readable storage medium having stored thereon a plurality of machine readable instructions, wherein when the instructions are executed by a system, the instructions provide ~~to configure~~ configuration of an integrated device in a processor or network component by:

decoding either a memory or IO configuration access to a configuration cycle; and

transmitting the configuration cycle to either a chipset or the integrated device, wherein the configuration cycle adheres to a ~~PCI~~ first type of interconnect protocol.

Claim 19 (currently amended): The article of manufacture of claim 18 wherein the chipset or integrated device is coupled to ~~the~~ a decoder of a first processor coupled to the processor or network component via a network fabric.

Claim 20 (currently amended): The article of manufacture of claim 18 wherein the ~~PCI~~ first type of interconnect protocol is in accordance with a ~~either~~ PCI ~~or PCI Express~~ type protocol.

Claim 21 (currently amended): A method for configuring an integrated device in a first processor comprising:

decoding a memory configuration access within a second processor, the second processor coupled to the first processor, to a configuration cycle; and

routing the configuration cycle from the second processor to a chipset and from the chipset to the first processor via a bridge to configure the integrated device from an unconfigured state to a configured state.